AMENDMENTS

In the Claims

1.   (Currently Amended) A method for updating microcode in an automated data storage library, comprising the steps of:

assigning a first <u>Logical Unit Number (</u>LUN<u>)</u> to a first device, wherein said first device is an Input/Output (I/O) device of said automated data storage library;

assigning a second LUN to a memory, wherein said memory is memory of said I/O device;

wherein said first LUN and said second LUN are separate, and wherein said first LUN processes I/O commands, and said second LUN processes microde update commands;

said first device receiving <u>a</u> ~~one or more~~ <u>plurality of</u> commands;

said first device obtaining a LUN address from each of said ~~one or more~~ <u>plurality of</u> commands;

in response to said LUN address obtained from each of said ~~one or more~~ <u>plurality of</u> commands being equal to said first LUN, processing each of said ~~one or more~~ <u>plurality of</u> commands as input/output commands of said first device; ~~and~~

in response to said LUN address obtained from each of said ~~one or more~~ <u>plurality of</u> commands being equal to said second LUN, directly overwriting said microde in said memory using said LUN address assigned to said memory by processing each of said ~~one or more~~ <u>plurality of</u> commands, thereby updating said stored microde in said first device<u>; and,</u>

<u>in response to said first device receiving a prepare for microcode update command, placing said first device in an operational state to receive said update of said microcode.</u>

2.   (Cancelled)

3.   (Cancelled)

4.   (Previously Presented) The method of claim 1, wherein said placing said first device in an operational state to receive said update of said microcode further comprises:

not accepting any new commands for processing;

completing all current commands; and

placing movable components at a rest position.

5.   (Currently Amended) The method of claim 1, wherein said processing each of said ~~one or more~~ plurality of commands to update said microcode further comprises:

overwriting said memory associated with said first device with an updated microcode.

6.   (Currently Amended) An automated data storage library for updating microcode, comprising:

a first device addressable by a first Logical Unit Number (LUN), wherein said first device is an Input/Output (I/O) device of said automated data storage library;

a memory addressable by a second LUN, wherein said memory is memory of said I/O device;

wherein said first LUN and said second LUN are separate, and said first LUN processes I/O commands, and said second LUN processes microde update commands and wherein said first device receives a ~~one or more~~ plurality of commands, obtains a LUN address from each of said ~~one or more~~ plurality of commands;

in response to said LUN address obtained from each of said ~~one or more~~ plurality of commands being equal to said first LUN, said first device processes each of said ~~one or more~~ plurality of commands as input/output commands of said first device; and

in response to said LUN address obtained from each of said ~~one or more~~ plurality of commands being equal to said second LUN, said first device:

directly overwrites said microde in said memory using said LUN address assigned to said memory by processing each of said ~~one or more~~ plurality of commands, and thereby updates said stored microde in said first device.

7.   (Original) The system of claim 6, further comprising:

a host, wherein said host sends microcode update commands to said first device.

8.   (Original) The system of claim 6, further comprising:

a host; and

a device interface coupled to said host, wherein said device interface receives commands

from said host and transfers said commands to LUN addressable components.

9.   (Original) The system of claim 6, wherein said memory is an Electrically Erasable Programmable Read Only Memory.

10.   (Original) The system of claim 6, wherein said memory is coupled to said first device.

11.   (Original) The system of claim 6, further comprising an accessor, wherein said memory is coupled to said accessor.

12.   (Original) The system of claim 6, further comprising:

a second device removably attached to said first device, wherein said memory is coupled

to said second device.

13.   (Original) The system of claim 6, further comprising:

a controller for operating said first device, wherein said memory is coupled to said

controller.

14.   (Cancelled)

15.   (Currently Amended) An article of manufacture comprising a computer readable medium tangibly embodying a program of machine-readable instructions executable by a digital processing apparatus to perform method steps for updating microcode of a first device of an automated data storage library assigned to a first Logical Unit Number (LUN), said first device coupled to a memory assigned to a second LUN, said method comprising the steps of:

said first device receiving a ~~one or more~~ plurality of commands, wherein said first device

is an Input/Output (I/O) Device, said memory is memory of said I/O device, said

first LUN and second LUN are separate, and wherein said first LUN processes

I/O commands, and said second LUN processes microde update commands;

in response to said LUN address obtained from each of said ~~one or more~~ plurality of

commands being equal to said first LUN, processing each of said ~~one or more~~

plurality of commands as input/output commands of said first device; ~~and~~

in response to said LUN address obtained from each of said ~~one or more~~ plurality of

commands being equal to said second LUN, directly overwriting said microde in

said memory using said LUN address assigned to said memory by processing

each of said ~~one or more~~ plurality of commands, thereby updating said stored

microde in said first device; and,

in response to said first device receiving a prepare for microcode update command,

placing said first device in an operational state to receive said update of said

microcode.

16.   (Cancelled)

17.   (Cancelled)

18   (Previously Presented) The article of manufacture of claim 15, wherein said

wherein said placing said first device in an operational state to receive said update of said

microcode further comprises:

not accepting any new commands for processing;

completing all current commands; and

placing movable components at a rest position.